

Status of Intel Atom Processor based Curriculum Enhancement at Howard University

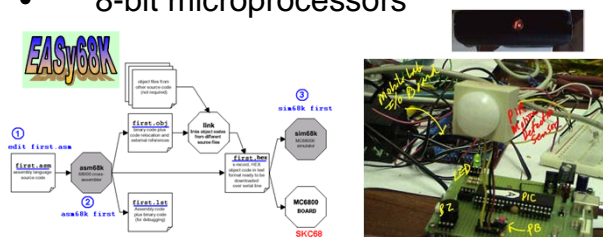
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- Intel Embedded Curriculum Grant: \$20,000 + 10 Tunnel Creeks + 1 Stellarton
- Project Period: June 2011 – May 2012
- Student Workers: Ravindranath Jaglal (Graduate) + Isaac Collins (Graduate) + Gerard Spivey (Undergraduate)

UNTIL SPRING 2011

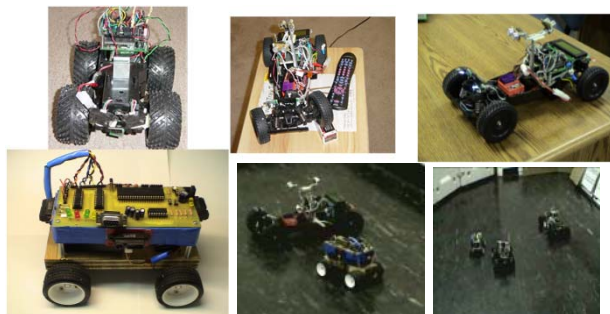
Fall Semester: EECE460 Microcomputer

- 8-bit microcomputers
- 8-bit microprocessors



Spring Semester: EECE456 Embedded Systems Design

- PIC based robot projects



FALL 2011

EECE460 Microcomputer

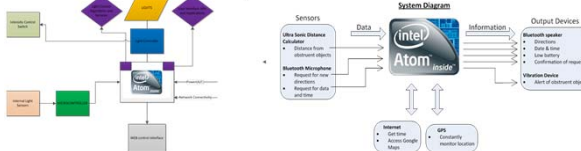
- 32-bit Intel Architecture
- Introduction of Intel Atom processor



SPRING 2012

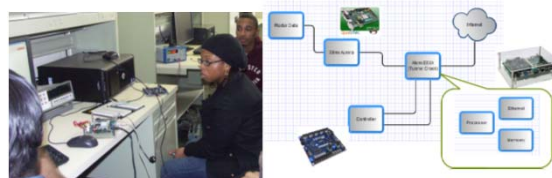
EECE 456 Embedded Systems Design

- Intel Cup Competition
 - Green Lighting (Main Team)
 - Blind Assistant (Wild Card Team)



Student Projects

- Video Reception from Swallable Capsule
- On-demand Fiber to Ethernet Data Conversion



Teaching Resource Development Project

- Carrier -Bot

