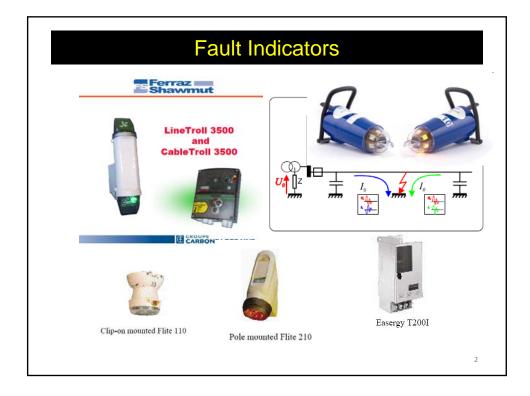
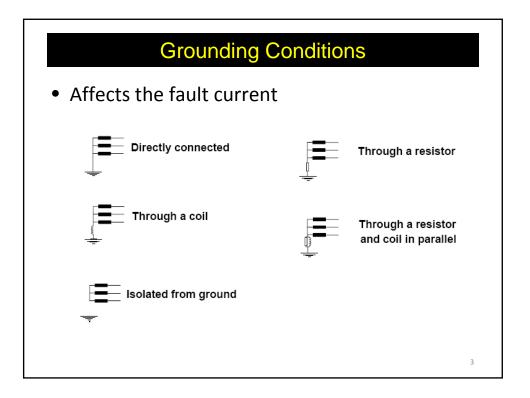
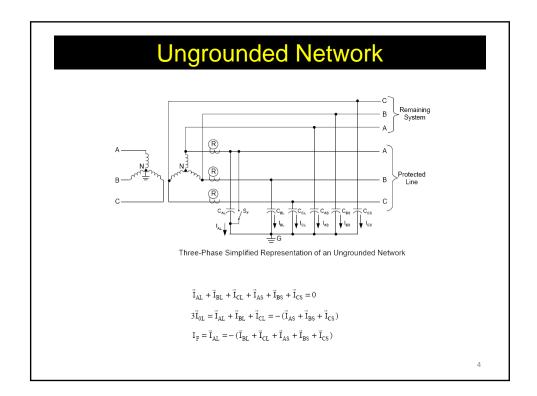
Source: www.mwftr.com

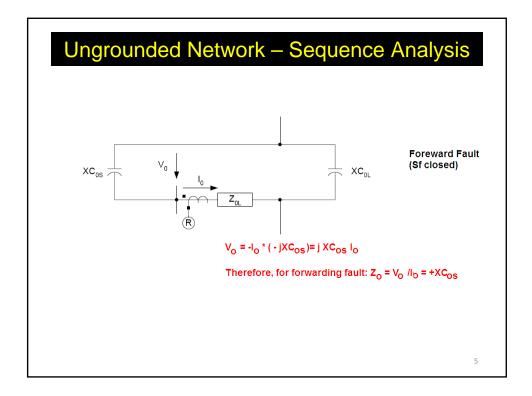
Charles Kim, "Lecture Notes on Fault Detection and Location in Distribution Systems," 2010.

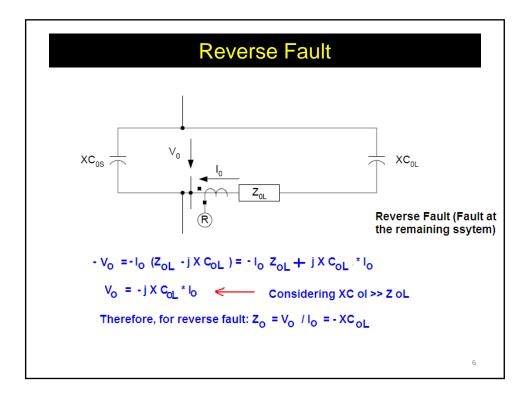
4. Fault Indicators	
Charles Kim	
Department of Electrical and Computer Engineering Howard University Washington, DC ckim@howard.edu	1

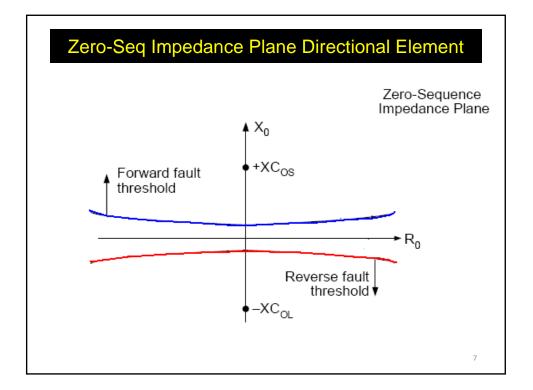


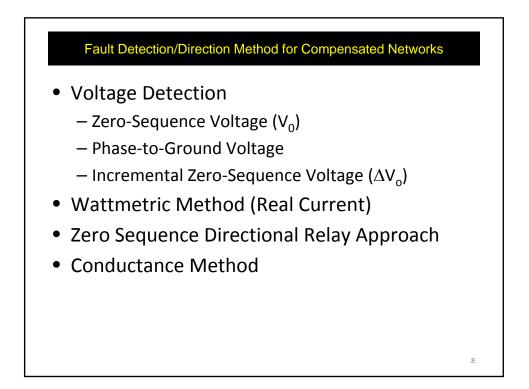


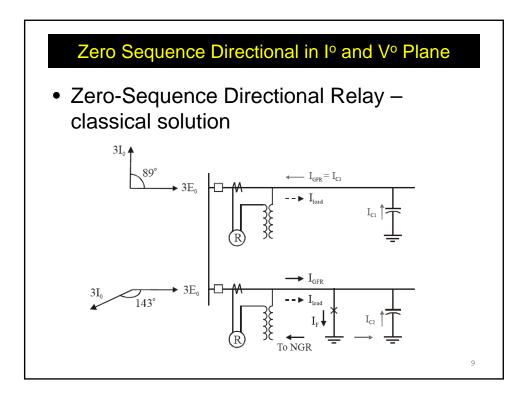


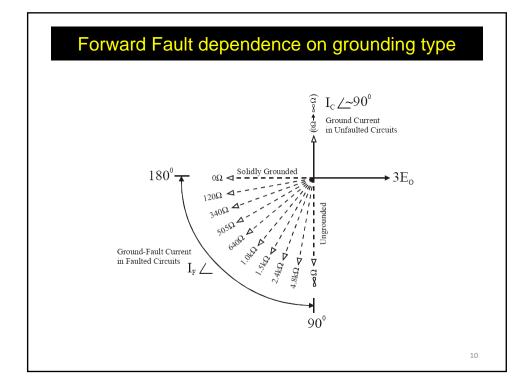


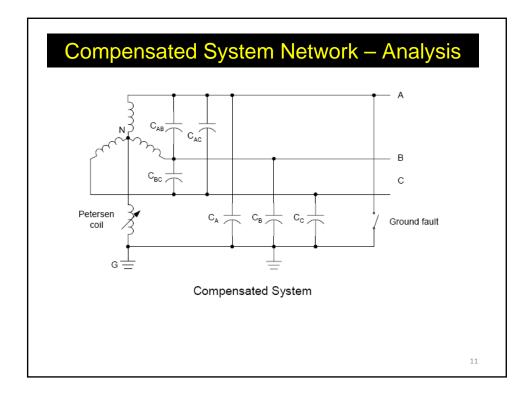




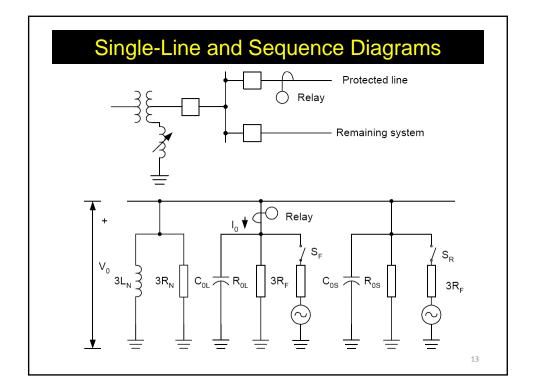


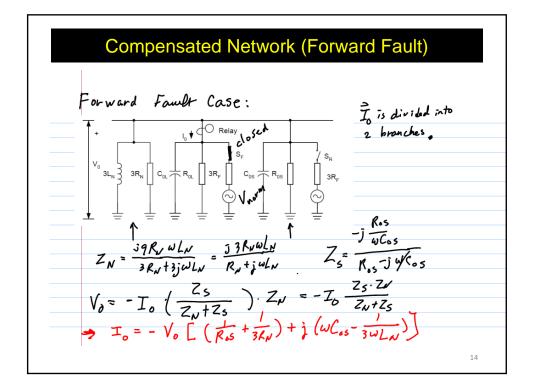


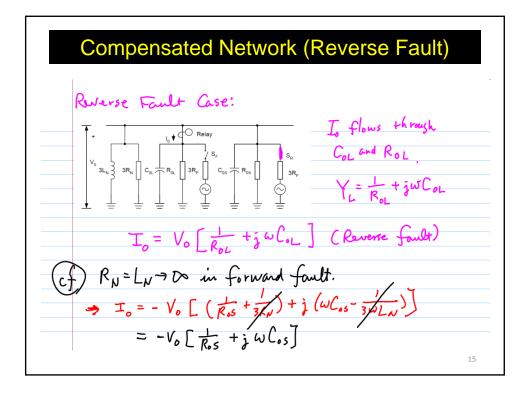


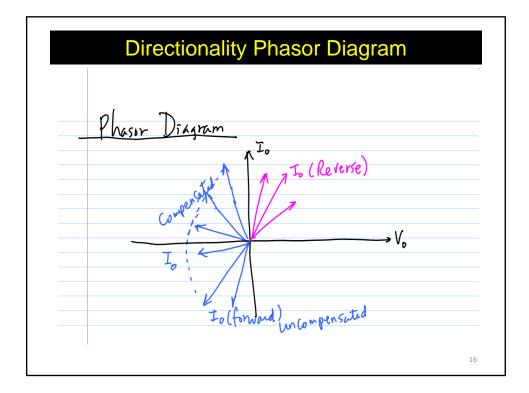


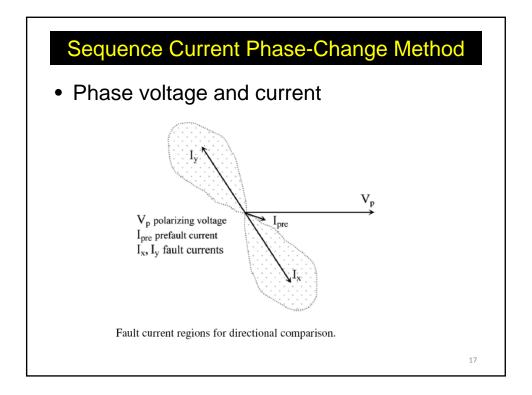
(12) United States Patent Roberts et al.		(10) Patent No.: US 6,573,726 B1 (45) Date of Patent: Jun. 3, 2003		
SYS ELE	SITIVE GROUND FAULT DETECTION TEM FOR USE IN COMPENSATED CTRIC POWER DISTRIBUTION WORKS	5,455,776 A * 10/1995 Novosel		
	ators: Jeffrey B. Roberts, Viola, ID (US); Daqing Hou, Pullman, WA (US); Hector Altuve-Ferrer, Nuevo Leon (MX) gnee: Schweitzer Engineering Laboratories, Inc., WA (US)	 * cited by examiner Primary Examiner—Safet Metjahic Assistant Examiner—Etienne LeRoux (74) Attorney, Agent, or Firm—Jensen & Puntigam, P.S. (57) ABSTRACT 		
1. A syste stribution means for zero se a calculat tance o sequen circuitry	laimed is: Im for detecting ground faults in a compen- network, comprising: determining the zero sequence voltage (V quence current (I_0) on a power line; ion system for calculating therefrom a co- r resistance value from the real parts of sai- ce voltage and zero sequence current; for enabling the operation of the calcu- for enabling the operation of the calcu- for enabling the operation of the calcu- for enabling the operation of the calcu- termination of the calculation of the calcula	$\begin{array}{c} \label{eq:constraint} \begin{tabular}{lllllllllllllllllllllllllllllllllll$		
means for against	for only preselected power line condition comparing the conductance or resistance a first threshold value to determine a fo nd a second threshold value to determ fault.	value prward		

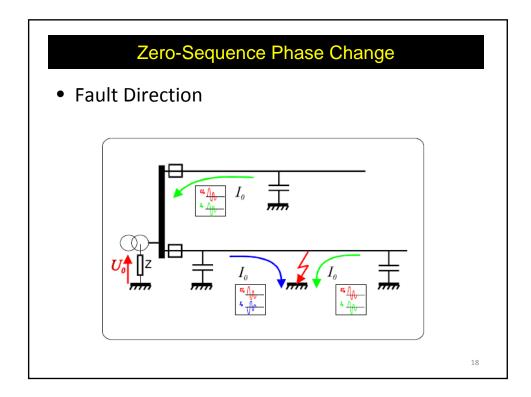


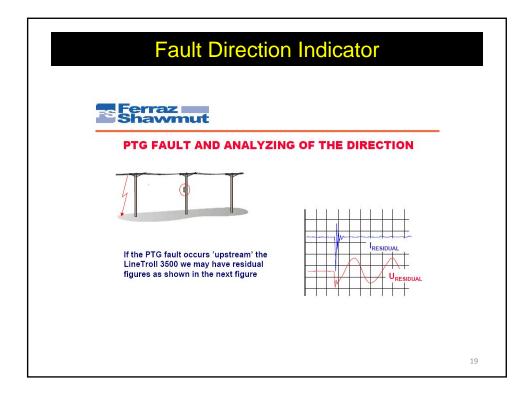


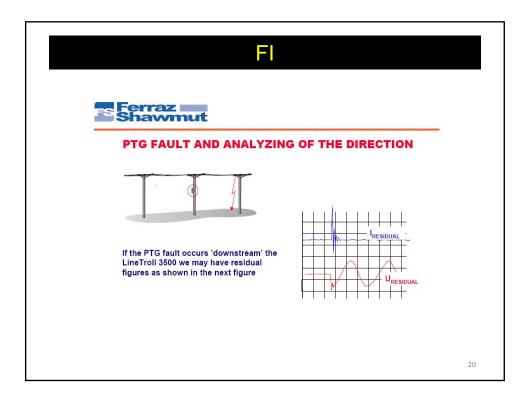


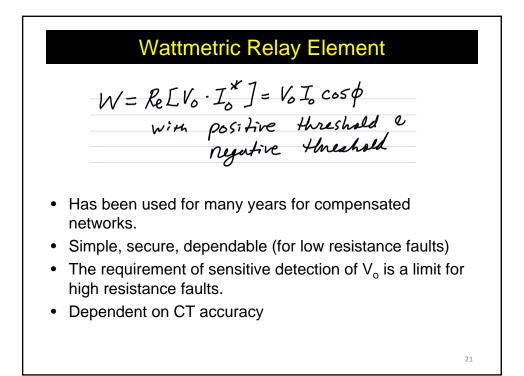


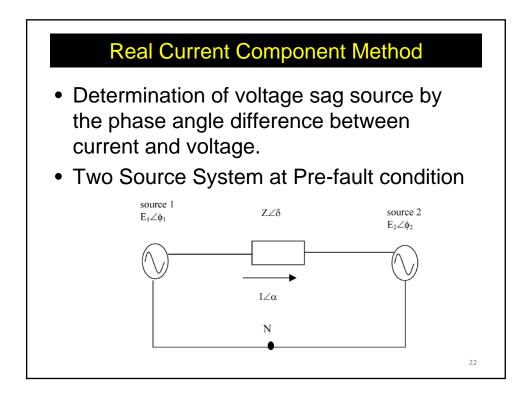


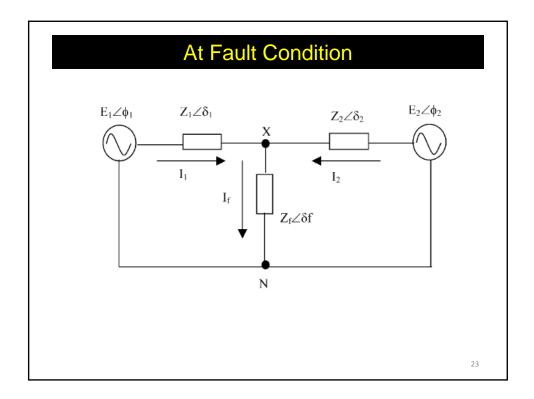


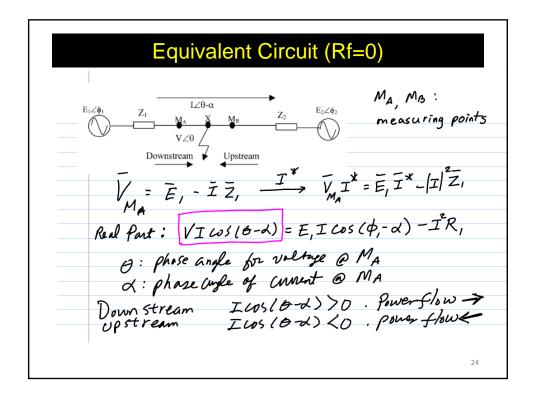


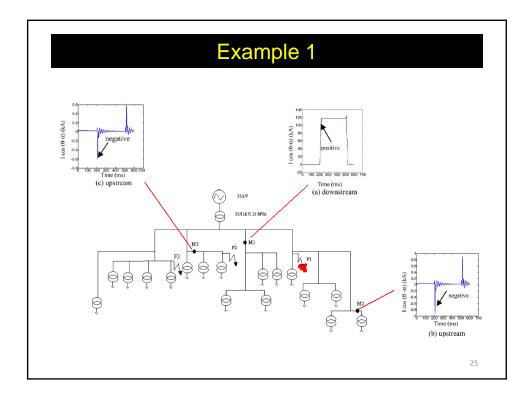


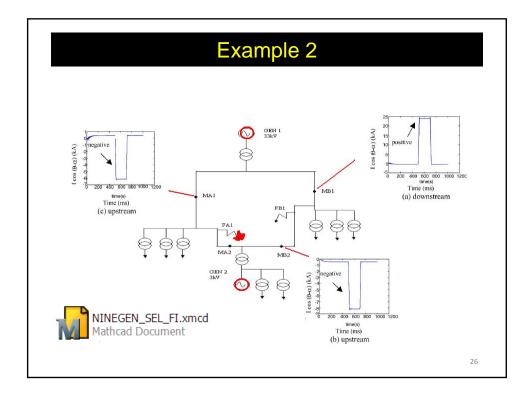


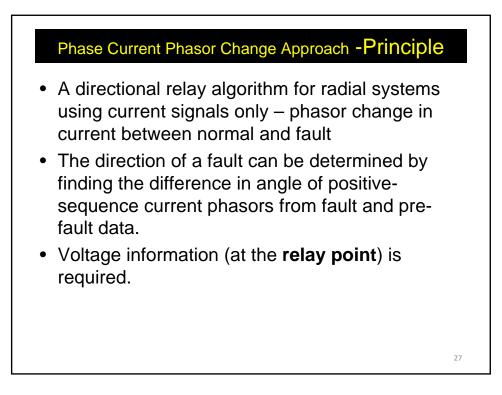


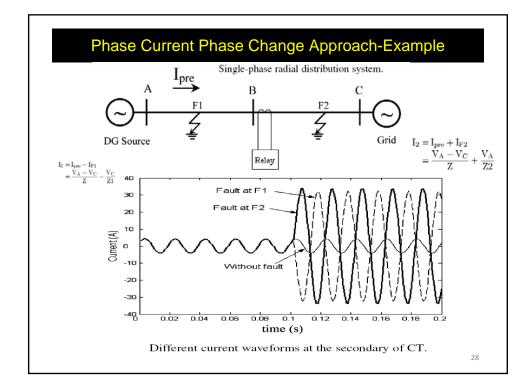


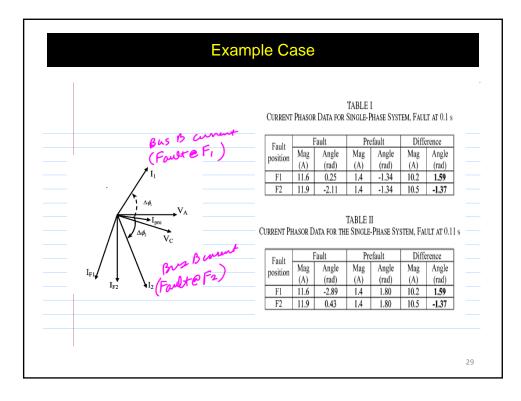




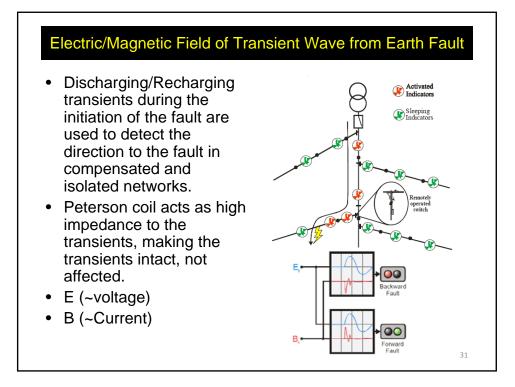


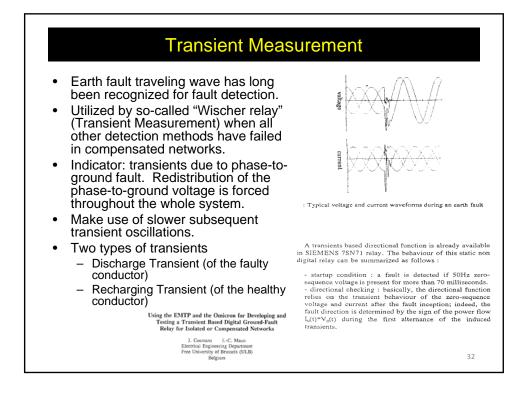






				US005796259A	
Ur	ited	States Patent [19]	[11]	Patent Number:	5,796,259
Dic	kmand	er	[45]	Date of Patent:	Aug. 18, 1998
[54] [75] [73]	DETEC Invento	ODS AND APPARATUS FOR TION OF FAULT DIRECTION	Attorney Mackiev [57] Apparati	Examiner—Glenn W. Brow Agent, or Firm—Woodd vicz & Norris LLP ABSTRACT us and methods for detecting n to a switch connected betw	cock Washburn Kurtz
[21]	Appl.	Generally, the invention de			
[22]	Filed:	on observations of the voltage fault inception instant. Mor			or compares un
[51]	Int. C	determines that fault direction			
[52]	U.S. (instant the polarity of the current deviation between the comparator			
[58]	Field	present cycle and the prior cy the measured voltage. For e positive polarity, a downstrea	cle is in p xample,	the same direction a if the voltage has	a in relation to des a fault incer-
[56]		cycle current to deviate from			
		positive direction. If the volt			a
	I	downstream fault will cause deviate from the prior cycle cu	une pres	ent cycle cullent i	

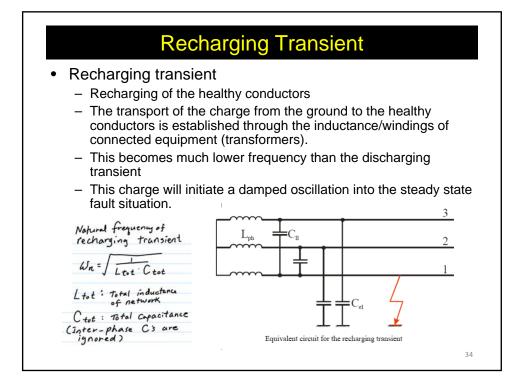


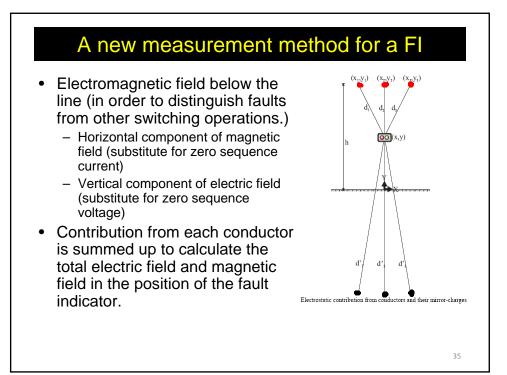


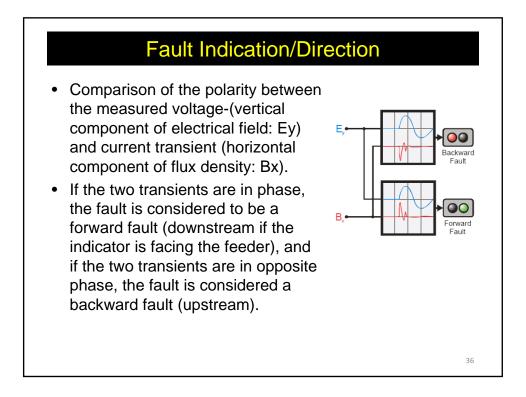
Discharging Transient

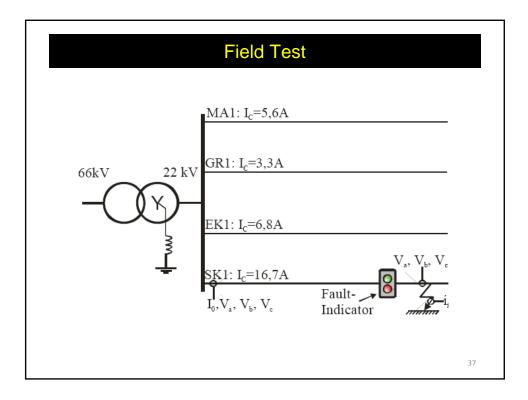
- Discharging transient
 - On faulty conductor
 - Charge is drained off
 - Ground is conducted to its entire length
 - Initial part of this charge is the traveling wave that passes along the faulty conductor and discharges it to ground.
 - The termination of the line ends determines the degree of reflection and damping
 - This transient is effectively damped out by skin-effect in cables and lines and by the load of the connected distribution transformer along the line.

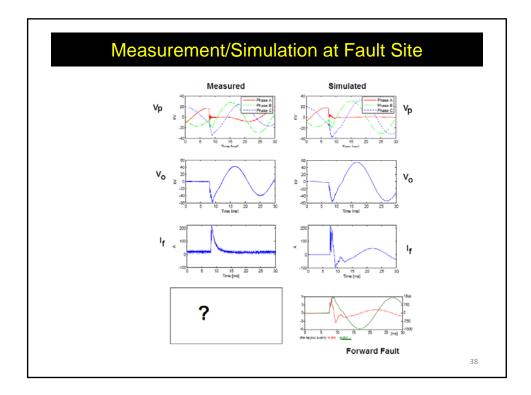
33

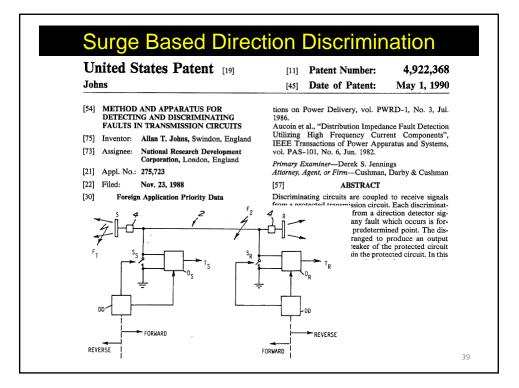


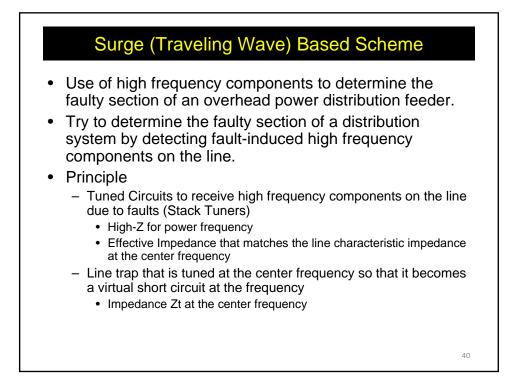


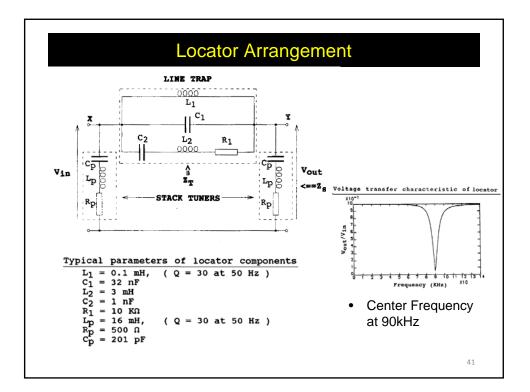


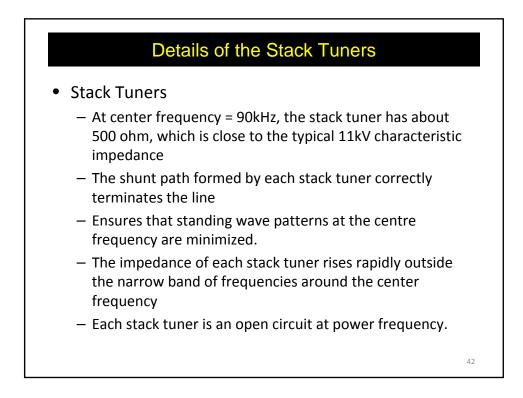


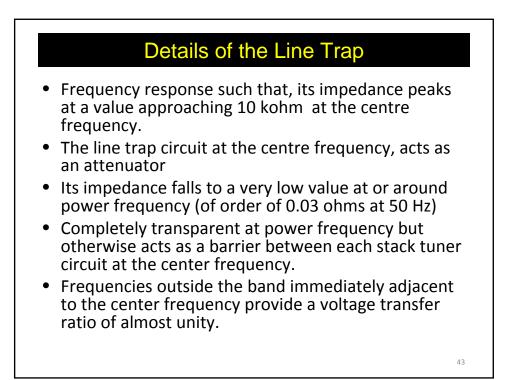


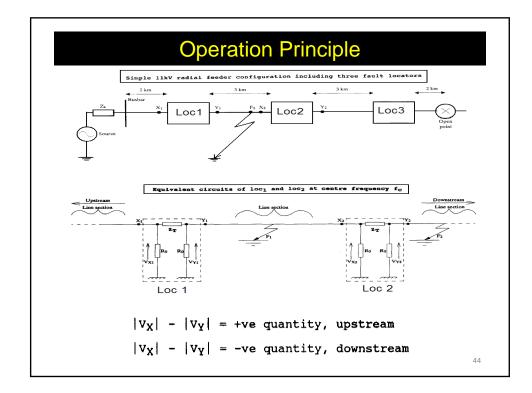


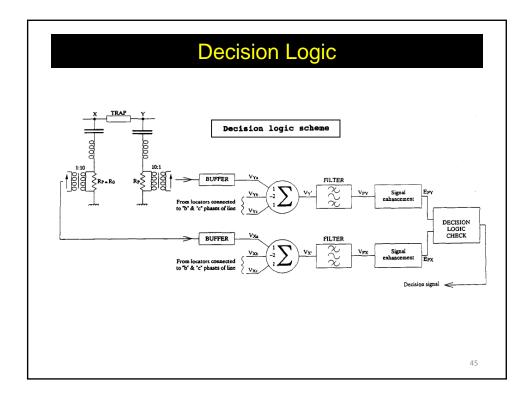




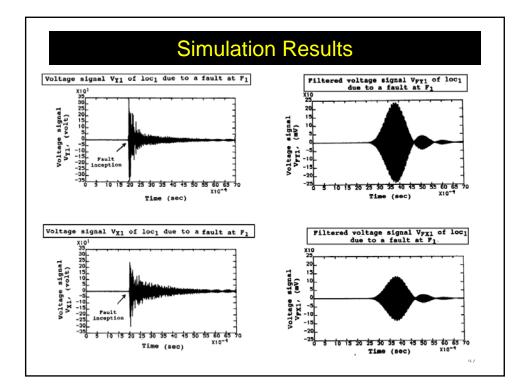


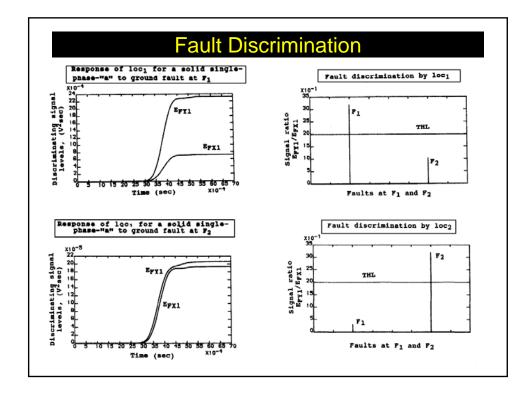


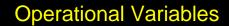




Simulation
 Data The source was represented by a simple lumped equivalent circuit with parameters set to produce a given symmetrical short circuit level at the bus-bar and a reactance to a resistance ratio of 30 at power frequency (50Hz). The ratio of the source zero to positive sequence impedance is unity and the equivalent power frequency impedance of the line is 0.54 + j0.64 ohms per Km (positive phase sequence) 0.69 + j2.02 ohms per Km (zero phase sequence) The sampling frequency was set at approximately 200 kHz thereby enabling the response of the locator to be examined for a center frequency of 90 kHz.
46







- Type of faults
- Fault resistance
- Fault Inception Angle
- Short Circuit Capacity of Bus-Bar (kVA level)

49

- Suggested Works
 - PSpice Simulation
 - Matlab/Simulink
 - MathCad Practice

